



Parameter	Ratings	Units
Blocking Voltage	350	V_P
Load Current	120	mA_{rms} / mA_{DC}
On-Resistance (max)	25	Ω

Features

- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 8-Pin DIP Package
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

The LAA100 is a 350V, 120mA, dual single-pole, normally open (1-Form-A) relay featuring low, 25 Ω , maximum on-resistance.

Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- EN/IEC 60950 Compliant

Ordering Information

Part #	Description
LAA100	8 Pin DIP (50/Tube)
LAA100P	8-Pin Flatpack (50/Tube)
LAA100PTR	8-Pin Flatpack (1000/Reel)
LAA100S	8-Pin Surface Mount (50/Tube)
LAA100STR	8-Pin Surface Mount (1,000/Reel)

Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings @25°C

Parameter	Ratings	Units
Blocking Voltage	350	V _P
Reverse Input Voltage	5	V
Input Control Current Peak (10ms)	50	mA
	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current	-	I _L	-	-	120	mA _{rms} / mA _{DC}
Peak	10ms	I _{LPK}	-	-	±350	mA _P
On-Resistance ²	I _L =120mA	R _{ON}	-	-	25	Ω
Off-State Leakage Current	V _L =350V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F = 5mA, V _L = 10V	t _{on}	-	-	5	ms
Turn-Off		t _{off}	-	-	5	ms
Output Capacitance	V _L = 50V, f=1MHz	C _{OUT}	-	35	-	pF
Input Characteristics						
Input Control Current to Activate	I _L = 120mA	I _F	-	-	5	mA
Input Control Current to Deactivate	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F = 5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R = 5V	I _R	-	-	10	μA
Common Characteristics						
Capacitance Input to Output	-	C _{IO}	-	3	-	pF

¹ If both poles operate, the load current must be derated so that the package power dissipation value is not exceeded.

² Measurement taken within 1 second of on-time.

PERFORMANCE DATA*

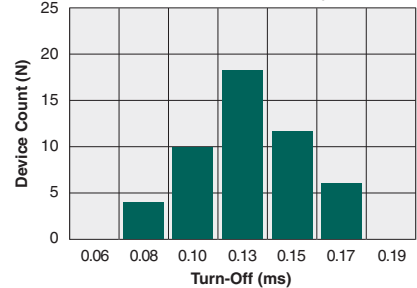
Typical LED Forward Voltage Drop
(N=50, $T_A=25^\circ\text{C}$, $I_F=5\text{mA}$)



Typical Turn-On Time
(N=50, $T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$, $I_F=5\text{mA}$)



Typical Turn-Off Time
(N=50, $T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$, $I_F=5\text{mA}$)



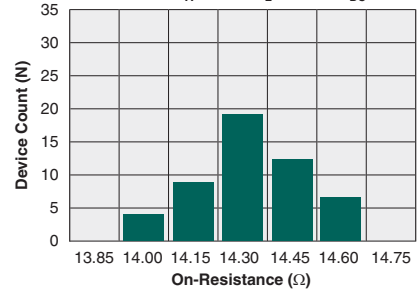
Typical I_F for Switch Operation
(N=50, $T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$)



Typical I_F for Switch Dropout
(N=50, $T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$)



Typical On-Resistance Distribution
(N=50, $T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$)



Typical Blocking Voltage Distribution
(N=50, $T_A=25^\circ\text{C}$)



Typical LED Forward Voltage Drop vs. Temperature



Typical Turn-On vs. LED Forward Current
($T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$)

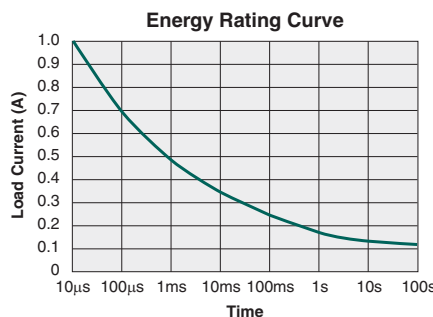
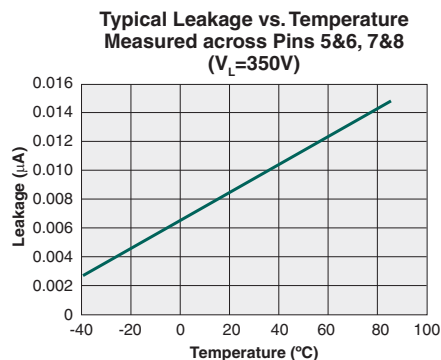
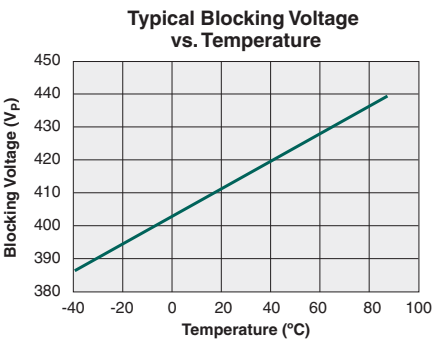
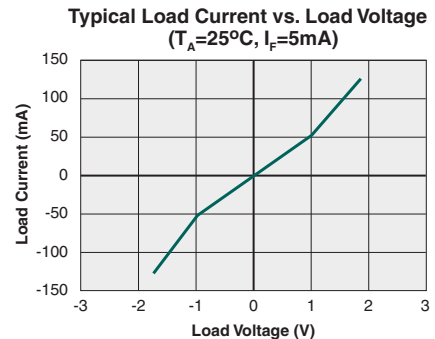
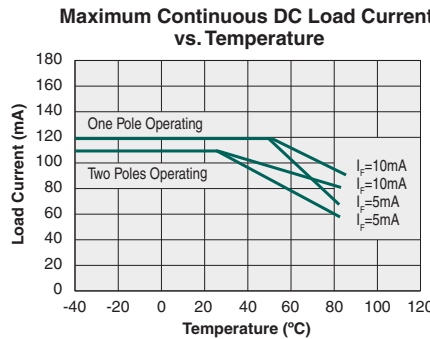
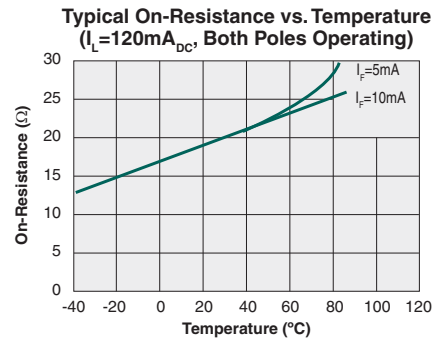
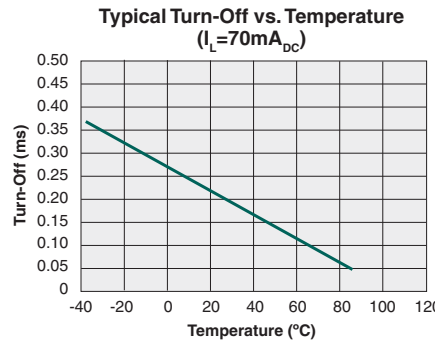
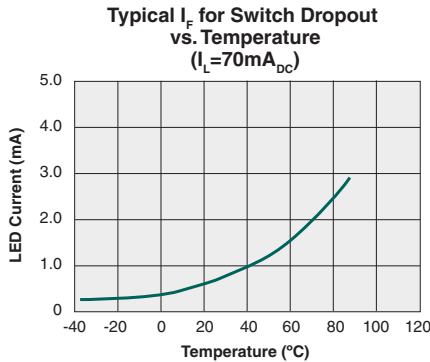


Typical Turn-Off vs. LED Forward Current
($T_A=25^\circ\text{C}$, $I_L=120\text{mA}_{\text{DC}}$)



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*



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Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
LAA100 / LAA100S / LAA100P	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
LAA100 / LAA100S	250°C for 30 seconds
LAA100P	260°C for 30 seconds

Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

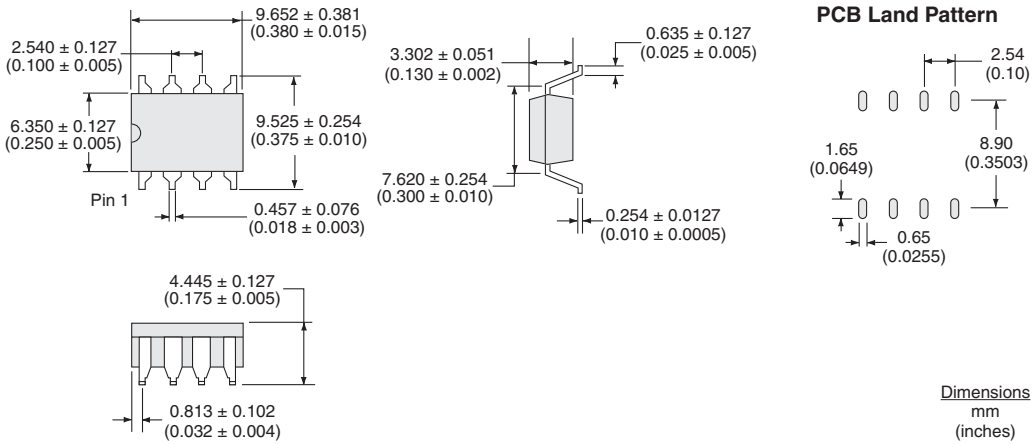


MECHANICAL DIMENSIONS

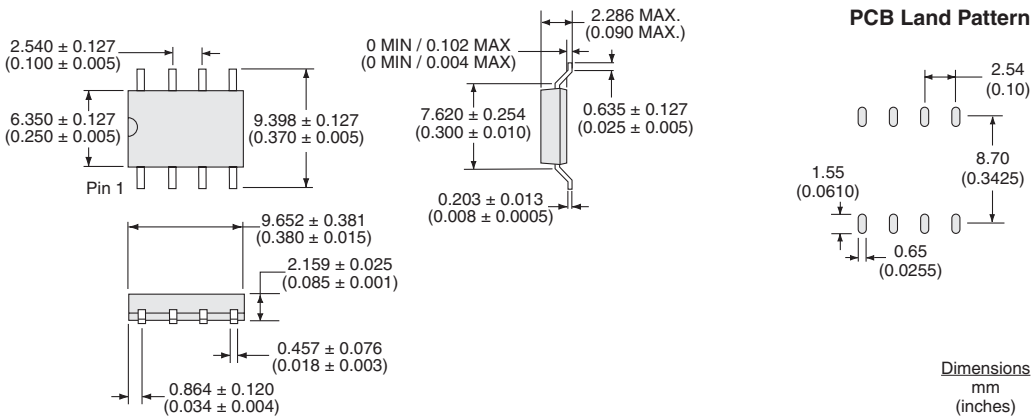
LAA100



LAA100S



LAA100P



LAA100STR Tape & Reel



LAA100PTR Tape & Reel



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Specification: DS-LAA100-R02
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